<u>**Main -Profile**</u>

Dr. SAKSHI RAJPUT

Assistant Professor, ECE, MSIT
Qualifications: Ph. D (ECE), M. Tech (VLSI Design), B. Tech (ECE), Gate Qualified
Contact No: :011-25528117
Email id: Sakshi.rajput10@msit.in (official)

ACADEMIC QUALIFICATION

DOCTOR of PHILOSOPHY: Electronics & Communication Engineering**, Uttarakhand Technical University**, Dehradun, **Govt.** (Dec 2021).

MASTER of TECHNOLOGY: (<u>**First Division with Distinction**</u>**)** (**VLSI DESIGN**), Guru Gobind Singh Indraprastha University (GGSIPU), Delhi, (2010-2012), Centre of Development of Advanced Computing, **C-DAC**, Scientific Society of the Ministry of Information Technology (formerly Department of Electronics), Government of India, Noida.

BACHELOR of ENGINEERING: (<u>**HONOURS**</u>**)** (Electronics & Communication Engineering), Rajasthan University (**RU, Jaipur**) (2005-2009), Institute of Engineering & Technology, Alwar (Rajasthan).

SENIOR SECONDARY EDUCATION: CBSE 2005, Rainbow English School, Janakpuri, Delhi.

SECONDARY EDUCATION: CBSE 2003, Rainbow English School, Janakpuri, Delhi.

EXPERIENCES & ACHIEVEMENTS

➢ Working as **"**<u>**ASSISTANT PROFESSOR**</u>**"** in ECE Department. in **"MAHARAJA SURAJMAL INSTITUTE of TECHNOLOGY" (MSIT)**, GGSIPU, Delhi since **07th August, 2012**, at PB - 3, Rs15600-39100 +AGP Rs 6000.

➢ Qualified **<u>GATE 2010</u>** in Electronics & Communication.

➢ Worked as **"**<u>**ASSISTANT PROFESSOR**</u>**"** in ECE Deptt. in **"MAHARAJA SURAJMAL INSTITUTE of TECHNOLOGY" (MSIT)**, GGSIPU, Delhi from 20 January, 2010 to 30 June, 2010.

➢ Received **<u>LETTER OF APPRECIATION</u>** for delivering lecture in 5days Lecture series from 18December 2017 to 22 December 2017 at MSIT.

➢ Received **<u>AWARD OF HONOUR</u>** for organizing Genesis 2018 held on 12th January 2018 at MSIT.

➢ **<u>UNIVERSITY TOPPER</u>** in **"ALGORITHM ANALYSIS and DESIGN, DIGITAL SYSTEM DESIGN WITH VERILOG, ALGORITHM FOR VLSI DESIGN ALGORITHM"** in **M. Tech** VLSI Design.

➢ **<u>COLLEGE TOPPER</u>** in **"VLSI DESIGN"** in VII sem, **"APPLIED ELECTRONICS"** in IV sem**, "CIRCUIT ANALYSIS, ELECTRICAL TECHNOLOGY"** in IInd sem of **B.E**.

➢ **<u>UNIVERSITY RANKER</u>** in **"ENGINEERING CHEMISTRY"** in **B.E** Ist year.

ADMINISTRATIVE EXPERIENCE

a) **Organized** FDP on "Digital Design using Verilog HDL" from 17th February. 2014 to 19th February, 2014.

b) **Organized** AICTE ISTE approved Orientation/Refresher Programme on "Use of ICT in Engineering Education" from 15t^h March to 20th March 2021 as **Co-coordinator**.

c) **Organized** AICTE ISTE approved Orientation/Refresher Programme on "Use of ICT in Engineering Education" from 7th April to 13th April 2021 as **Team Member**.

d) **Organized** AICTE ISTE approved Orientation/Refresher Programme on "Use of ICT in Engineering Education" from 24th February to 2nd March 2021 as **Team Member**.

e) **Satyam Editor** (2012-2016, 2021-presently): SATYAM is the Research journal of MSIT, which started in2012year.Iwas the core team member who have started this research journal and worked as the Editor-in-chief of the journal for 5 consecutive years.

f) **Departmental Purchase Committee Member** (2021 till present): Establish Ist Complete Interactive Smart Classroom solution in 206 room. Second complete smart interactive solution for conference room. Purchase systems, printer, hard disk etc for lab, faculty members, library, and different cells in MSIT College.

g) **Committee for Requirement of Computer System Member**: Requirements calculation, managing of Computer systems for future purchase.

h) **ISO Certified Internal Auditor** (2014 till present): Auditing different departments quality work as per ISO standards.

i) **Annual day** organizing active team member 2013, 2014, 2015.

j) **Genesis** 2017, 2018 organizing active team member.

k) Other than the above major responsibilities I have also handled various other responsibilities at the department level and as a team member like **Project Co-coordinator, Proctor, Mentor, Sports and Cultural activities, Tech fest, Admission, Teacher diary designing, Monthly Academic progress report, Lab in charge, FDPs Participation Record Maintainer member**.

l) VLSI Lab Manual Revision **In charge**

m) **Coordinator** for developing teaching resource for subject ES

n) EIM lab Manual Revision **Team Member**.

o) **ECE Department Academic Committee member** of ECE since 2020 till present.

p) ECE NBA criteria 4 **Team Member** since 2018 till present.

q) ECE **Industrial Visit- In charge** 2014-15 for consecutive two years.

r) **Departmental PTM In charge** in 2019-2020.

s) Study webs of active learning for young aspiring minds (**SWAYAM**) Committee member.

t) **University Subject Coordinator**: Assigned the responsibility of setting paper for all the affiliated colleges of GGSIP University many times for the following subjects: VLSI Design, STLD, Embedded System, and Digital System Design.

u) **Editor, Vision (2016):** Vision is the college Annual magazine released by MSIT. I was responsible for doing every work required for printing the magazine which includes inviting various technical and non-technical articles, designing the pages, inviting quotations for printing the college magazine and going through the whole process of printing the magazine.

PUBLICATIONS

JOURNAL PUBLICATION	12
CONFERENCE PUBLICATION	05
BOOK PUBLICATION	04

COURSES TAUGHT AT UG LEVEL

- VLSI Design
- Digital System Design
- EIM
- Analog Electronics
- STLD

WORKSHOP/FDP/SEMINAR/TRAINING/INDUSTRIAL TRAINING/ COURSE ATTENDED

1. **Trainings and FDPs**

S No.	Title of QIP/FDP/Workshop	Name of Training Institution, City	Type of Training and FDPs	Period	
				From	To
1	C & C++	Rama Institute of Internet Technologies,Delhi	3 month Certificate Course	June 01,2006	August 31, 2006
2	Wireless Communication Emerging Technologies	Yonsei University, Coursera	1month Certificate Course	June 04, 2017	July 13, 2017
3	Internet Emerging Technologies	Yonsei University, Coursera	1month Certificate Course	March 08,2017	April 05,2017
4	JAVA	Rama Institute of Internet Technologies,Delhi	1month Certificate Course	Aug 03,2007	Sep 03, 2007
5	Deep Learning and Open CV	Brain Mentor, Delhi	Industrial Training	Nov 22,2022	Dec 03, 2022
6	Machine Learning using Python	Brain Mentor, Delhi	FDP	March 2, 2020	March 6, 2020
7	Recent Trends in Artificial Intelligence and Its Applications	MSIT,Delhi	FDP	Nov 29,2021	Dec 3 , 2021
8	Internet of Things (IoT)-A Practical Approach	AICTE(ATAL) Academy	FDP	Dec 06,2021	Dec 10,2021
9	ICT tool for Teaching	GBPIET Uttrakhand	FDP	Dec 14 2020	Dec 18 2020
10	Open Source Technologies through	NITTTR Chandigarh, held at MSIT, Delhi	FDP	dEC 03, 2018	DEC 07, 2018

	ICT				
11	Machine Learning using Python	MSIT, Delhi	FDP	March 2, 2020	March 6, 2020
12	Wireless Propagation	NITTTR Chandigarh, held at MSIT, Delhi	FDP	Feb 25, 2019	March 1, 2019
13	Use of ICT in Engineering Education	AICTE ,ISTE	FDP	Feb 24, 2021	March 2, 2021
14	Modeling & Simulation using MATLAB	NITTTR Chandigarh, held at MSIT, Delhi	FDP	May 21, 2018	May 25, 2018
15	4G and 5G	NITTTR Chandigarh, held at MSIT, Delhi	FDP	17Sep, 2018	21 Sep, 2018
16	Power Electronics & Its applications	NITTTR Chandigarh, held at MSIT, Delhi	FDP	27/11/2017	01/12/2017
17	Artificial Neural Network	NITTTR Chandigarh, held at MSIT, Delhi	FDP	April 25 2016	April 29, 2016
18	MATLAB and SCI Lab	NITTTR Chandigarh, held at MSIT, Delhi	FDP	Sep 18 ,2017	Sep 22,2017
19	Sim4Net	Amity University, Jaipur	FDP	Dec 04, 2017	Dec 8, 2017
20	Digital Design Using Verilog HDL	Trident Tech Labs Pvt. Ltd. , MSIT,Delhi	FDP	Feb 17,2019	Feb 19,2019
21	Virtual Reality and Digital Design Techniques	MSIT ,Delhi	FDP	Feb 28,2015	Feb 28,2015
22	Research Methods and Data Analysis using SPSS	MSIT ,Delhi	Workshop	Nov 09,2012	Nov 10,2012
23	Antenna Designing & Measurements	MSIT ,Delhi	Workshop	March 01,2019	March 02, 2019
24	Embedded System and Robotics	Ritusha Consultant Pvt. Ltd.,MSIT,Delhi	Workshop	March 31, 2014	April 01,2014
25	Antenna Designing using ADS	MSIT ,Delhi	Workshop	October 09,2017	October 09,2017
26	Recent Trends in Semiconductor Devices and Technology	Deen Dayal Upadhyaya College University of Delhi, & IEEE-EDS ,Delhi	Workshop	January 19,2013	January 20,2013
27	EFY Expo on Test &Measurements	Pragati Maidan	Workshop	Feb 13, 2013	Feb 13, 2013
28	Internal Quality Auditors	Training Programme On Quality Management System	Training	Dec 17, 2013	Dec 17, 2013

		As Per ISO 9001:2008, MSIT			
29	Paradigm Shift from Developing to Developed India through -Digitalization	MSI, Delhi	National Conference	July 28,2017	July 29,2017
30	Recent Developments in Electronics	IEEE-EDS, Delhi	National Conference	January 18,2013	January 20,2013
31	Artificial Intelligence and Applications	MSIT, Delhi	International Conference	Feb 06,2020	Feb 07,2020
32	Extropianism :Towards Convergence of Human Values and Technology	Ansal University, Gurgaon	International Conference	January 18,2013	January 19,2013
33	NCECS 2013	Inderprastha Engineering College, Ghaziabad	National Conference	April 05,2013	April 06,2013
34	IOT using Ardino	APTRON, Delhi	Seminar	Aug 18, 2017	Aug 18, 2017
35	MATLAB & Simulink for Engineering Education	Mathworks India	Seminar	September 24, 2013	September 24, 2013
36	Virtual Lab	MSIT, Delhi	Seminar	Feb 05, 2013	Feb 05, 2013
37	Advances In Communication Technology	MSIT, Delhi	Seminar	04 March ,2013	04 March ,2013
38	Emerging Trends in Embedded Industry	Mr. Anurag Pratap Singh, ST Microelectronics MSIT, Delhi	Seminar	Oct 05, 2013	Oct 05, 2013

(Dr. SAKSHI RAJPUT)