**Faculty Profile**

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| Name | :Dr. Sakshi Rajput |
| Designation | : Assistant Professor |
| Qualifications | : Ph.D(Wireless Communication), |
| | M.Tech.(VSI Design) , B.E (ECE) |
| Phone | :**011-25528117** |
| Email | :Sakshi.rajput10@msit.in |

Area of Interest/Specialization: VLSI Design (Analog /Digital),

Self Organizing network, Deep Neural Network, Mobility Protocol management

Experience                  : 10 years

## ACADEMIC QUALIFICATION

1) **Doctor of Philosophy:** 2021: Uttrakhand Technical University.

2) **MASTER of TECHNOLOGY: (HONOURS)** (VLSI DESIGN), Guru Gobind Singh Indraprastha University (GGSIPU), Delhi, (2010-2012), **C-DAC**, Noida.

3) **BACHELOR of ENGINEERING: (HONOURS)** (Electronics & Communication Engineering), Rajasthan University (RU, Jaipur) (2005-2009), Institute of Engineering & Technology, Alwar (Rajasthan).

4) **SENIOR SECONDARY EDUCATION:** CBSE 2005, Rainbow English School, JanakPuri, Delhi.

5) **SECONDARY EDUCATION:** CBSE 2003, Rainbow English School, JanakPuri, Delhi.

## EXPERIENCES & ACHIEVEMENTS

1) Working as **"ASSISTANT PROFESSOR"** in ECE Deptt. In **"MAHARAJA SURAJMAL INSTITUTE of TECHNOLOGY" (MSIT)**, GGSIPU, Delhi since 07th August, 2012.

2) **GATE 2010** Qualified in Electronics & Communication.

3) Worked as **"ASSISTANT PROFESSOR"** in ECE Deptt. In **"MAHARAJA SURAJMAL INSTITUTE of TECHNOLOGY" (MSIT)**, GGSIPU, Delhi from 20 January, 2010 to 30 June, 2010.

4) **UNIVERSITY TOPPER** in **"ALGORITHM ANALYSIS and DESIGN, DIGITAL SYSTEM DESIGN WITH VERILOG, ALGORITHM FOR VLSI DESIGN ALGORITHM"** in **M.Tech** VLSI Design.

5) **COLLEGE TOPPER** in **"VLSI DESIGN"** in VII sem, **"APPLIED ELECTRONICS"** in IV sem**, "CIRCUIT ANALYSIS, ELECTRICAL TECHNOLOGY"** in IInd sem of **B.E**.

6) **UNIVERSITY RANKER** in **"ENGINEERING CHEMISTRY"** in **B.E** Ist year.

7) **EDITOR** of MSIT Multidiscipline biannual research journal "**SATYAM**" with an ISSN No: 2319-7897.

## PUBLICATION

1) "**SELF ORGANIZING DISTRIBUTED MOBILITY MANAGEMENT FOR NEXT GENERATION MOBILITY PROTOCOL**" in JOURNAL OF AMBIENT INTELLIGENCE AND HUMANIZED COMPUTING, January 2021, https://doi.org/10.1007/s12652-020-02770-w. **Author: Sakshi Rajput, Kamaljit Bhatia. Impact Factor : 7.104(2020).**

2) "**ANALYZING HANDOVER PERFORMANCE OF MOBILITY MANAGEMENT PROTOCOL USING NEURAL NETWORK**" in JOURNAL OF AMBIENT INTELLIGENCE AND HUMANIZED COMPUTING, January 2021, https://doi.org/10.1007/s12652-020-02822-1. **Author: Sakshi Rajput, Kamaljit Bhatia. Impact Factor : 7.104(2020).**

3) **Sakshi Rajput**, Rohit Shukla, , Ankit Anand, "Implementation of High Speed and Low Power Hybrid Adder Based Novel Radix 4 Booth Multiplier" in **IEEE International Conference on Communication Systems and Network Technologies**, 2013, April 2013.

4) **Sakshi Rajput**, Priya Sharma, Garima,Gitanjali, "Implementation of High Speed and Low Power Novel Radix 2 Booth Multiplier using 2248 BEC Converter" in **IEEE Conference on Recent Developments in Electronics**, January 2013.

5) **Sakshi Rajput** , , Ankit Anand ,"Hardware Implementation of RSA Algorithm" in **International Conference on Extropianism: Towards Convergence of Human Values and Technology**, January 2013,

6) **Sakshi Rajput** ,Priya Sharma, Garima, Gitanjali,"Implementation of Compact and Power Efficient BEC Based Novel Carry Select Adder" in **International Conference on Extropianism: Towards Convergence of Human Values and Technology**, January 2013.

7) **Sakshi Rajput**, , Ankit Anand "8-Bit ALU Using 16:1 Multiplexer" **National Conference on Electronics & Communication System (NCECS-2013)**, April 5th-6th, 2013.

8) **"Design and Comparative Study of CMOS Charge Pump for Low Voltage Application using CTS Logic"** in International Journal on Electronics & Communication Technology, March 2013. ISSN: 2230-7109. **Author:Sakshi Rajput,Ankit Anand,Pushkar Praveen. Pages:03**

9) **"Design and Implementation of High Pumping Efficiency CMOS Charge Pump"** in International Journal on Computer Science and Technology ISSN: 0976-8491, March 2013. **Author:Sakshi Rajput. Pages:04**

10) **"High Speed and Reduced Power – Radix-2 Booth Multiplier"** in International Journal of Computational Engineering & Management, Volume 16 Issue 2, and March 2013.ISSN: 2230-7893. **Author: Sakshi Rajput,Gitanjali,Priya Sharma,Garima. Pages:07**

11) **"Design of Low Power and High Speed BEC 2248 Efficient Novel Carry Select Adder"** in International Journal of Advances in Engineering & Technology, March 2013.ISSN: 2231-1963. **Author:Sakshi Rajput,Gitanjali,Priya Sharma,Garima. Pages:07**

12) **"Implementation of Power Efficient Novel Multiplexer Based Arithmetic Logic Unit"** in International Journal of Engineering Research and Technology, Feb, 2013.ISSN: 2278-0181. **Author:Sakshi Rajput. Pages:03**

13) **"Design and Implementation of CTS CMOS Charge Pump"** in International Journal on Computer Science and Technology ISSN: 0976-8491, March 2013. **Author:Sakshi Rajput. Pages:04.**

14) **"IMPLEMENTATION OF SHORTEST PATH ALGORITHM FOR RECTILINEAR STEINER TREE PROBLEM"** in International Journal of Research in Computer Application & Management. **Author:Sakshi Rajput. Pages:04**

## FACULTY DEVELOPMENT PROGRAME

| S No. | Name of Training | Name of Training Institution, City | Type of Training and FDPs | Period | |
|---|---|---|---|---|---|
| | | | | From | To |
| 1 | Machine Learning using Python | NITTTR Chandigarh, held at MSIT, Delhi | FDP | March 2, 2020 | March 6, 2020 |
| 2 | ICT tool for Teaching | GBPIET Uttrakhand | FDP | Dec 14 2020 | Dec 18 2020 |
| 3 | Open Source Technologies through ICT | NITTTR Chandigarh, held at MSIT, Delhi | FDP | DEC 03, 2018 | DEC 07, 2018 |
| 4 | Wireless Propagation | NITTTR Chandigarh, held at MSIT, Delhi | FDP | Feb 25, 2019 | March 1, 2019 |
| 5 | Use of ICT in Engineering Education | AICTE ,ISTE | FDP | Feb 24, 2021 | March 2, 2021 |
| 6 | Modeling & Simulation using | NITTTR Chandigarh, held at MSIT, Delhi | FDP | May 21, 2018 | May 25, 2018 |

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| | MATLAB | | | | |
| 7 | 4G and 5G | NITTTR Chandigarh, held at MSIT, Delhi | FDP | 17Sep, 2018 | 21 Sep, 2018 |
| 8 | Power Electronics & Its applications | NITTTR Chandigarh, held at MSIT, Delhi | FDP | 27/11/2017 | 01/12/2017 |
| 9 | Artificial Neural Network | NITTTR Chandigarh, held at MSIT, Delhi | FDP | April 25 2016 | April 29, 2016 |
| 10 | MATLAB and SCI Lab | NITTTR Chandigarh, held at MSIT, Delhi | FDP | Sep 18 ,2017 | Sep 22,2017 |
| 11 | Sim4Net | Amity University, Jaipur | FDP | Dec 04, 2017 | Dec 8, 2017 |