

Maharaja Surajmal Institute of Technology, New Delhi
Electrical and Electronics Engineering Department

A Report on

“PCB DESIGNING RESEARCH INTERNSHIP”

The last session of the PCB Designing workshop hosted by the Electrical and Electronics Engineering Department of Maharaja Surajmal Institute of Technology, led by Mr. Shayak Chaudhari and Mr. Vaibhav Bhardwaj concluded with a note from the speakers giving an opportunity to the attendees of the workshop to work as a research intern at IIT Delhi's EMPACT research lab.

The attendees had been required to fill out a Google form to show interest in this opportunity. As for shortlisting the candidates for the interview, there was no such criterion except that the candidates should have attended enough workshop sessions for them to be able to work on KiCad or any other PCB designing software efficiently.

The interviews were held on 24th May 2024 in room 306A in MSIT, led by Mr. Shayak Chaudhari and Mr. Vaibhav Bhardwaj. There had been a total of 21 candidates appearing for the interview. The candidates were asked basic questions about what they had learned in the workshop, a synopsis of the projects they had mentioned in their resume, and also some fundamental questions about what the candidates had been taught in their electrical and electronics engineering course. The candidates were also asked whether they would be able to carry their laptops and be able to travel without inconvenience to IIT Delhi five days a week. The interviewers mentioned that the work to be done at the lab calls for dedication and determination and that taking unnecessary leaves during the tenure of the internship would do no good to the candidate in terms of learning and might even debar the candidate from getting a completion certificate.

After about three hours of interviews with the candidates, Mr. Shayak Chaudhari and Mr. Vaibhav Bhardwaj released the results for the selected candidates later in the day. Akshat Kareer, Amitoj Kaur, and Vijayant from 3rd year EEE, and Nirbhay Tiwari from 2nd year EEE were the four candidates selected as research interns and would be working at IIT Delhi's EMPACT research lab for six weeks starting from July 1st, 2024.