Region 10

IEEE PES MSIT Student Chapter

EVENT REPORT

Seminar on Hardware Digital Design

Chapter Advisor	Chairperson	Vice-Chairperson	General Secretary	Treasurer	PR Representative
Dr. Jyoti Jain	Chetan Wadhwa	Pranav Sood	Ishika Deshwal	Akriti Kumari	Rohit Rajput

POSTER OF THE EVENT

Date: 1 April 2025
Time: 11:00AM -12:00PM IST
Location: Seminar Hall 206, MSIT

- The IEEE Power and Energy Society (PES) of MSIT successfully organized an exclusive seminar aimed at bridging the gap between theoretical knowledge and real-world hardware applications. The seminar, titled *"**Hardware Digital Design – Schematic Design, HDL, Verilog & FPGA Implementation**"* focused on imparting practical knowledge to students of ECE and EEE departments.

- The event featured **Mr. Vaibbhav Mishra**, a renowned name in the tech industry. He is the Founder of **PinE Training Academy** and the Co-Founder of **Eleve8** and **Aujus Technology**. With years of industry experience and deep technical expertise, Mr. Mishra brought a wealth of knowledge on digital hardware design, VLSI, and embedded systems.

- The seminar commenced with a warm welcome address by **Ms. Akriti Kumari**, Treasurer of IEEE PES, who introduced the audience to the objective of the session and welcomed the speaker. Following this, **Mr. Chetan Wadhwa**, Chairperson of PES, presented Mr. Mishra with a bouquet as a token of gratitude and appreciation.

- Mr. Mishra began the session by introducing himself and sharing his professional journey. He then emphasized the growing relevance of **electronics and semiconductors** in shaping the future, highlighting how essential these fields are in today's technological advancement.

- The core focus of the seminar was on **Hardware Digital Design**, where the speaker gave hands-on insights and real-world applications related to:
Schematic Design
Hardware Description Languages (HDL)
Verilog Programming
FPGA (Field-Programmable Gate Array) Implementation

- A major highlight of the seminar was Mr. Mishra's focus on semiconductors and their growing importance in the global tech ecosystem. He explained how the semiconductor industry is experiencing a boom, with immense opportunities for innovation and employment. He also discussed India's evolving role in **semiconductor manufacturing and design**.He also introduced students to the current tools and platforms used in the VLSI and embedded systems industry, making the session highly relevant for aspiring engineers.

- Students attending the seminar gained multifaceted benefits including:

1.**Industry-Ready Skills**: Exposure to industry-grade tools and processes helped students understand what is expected in real job roles related to VLSI and embedded systems.

2.**Expert Insights**: The speaker clarified complex technical topics and shared his knowledge of evolving trends in hardware design.

3.**Career Guidance**: Mr. Mishra also shared tips on how students can build a successful career in the field of digital hardware and embedded technologies.

4.Networking: The event offered students the opportunity to connect with peers, faculty, and the guest speaker.

- Towards the end of the seminar, an **interactive Q&A session** was held where students were encouraged to ask doubts related to the seminar topic. Mr. Mishra enthusiastically addressed each query, providing valuable insights and motivating students to pursue research and development in this field.

- Mr. Mishra concluded the session with an inspiring message to all students. He stressed the importance of continuous learning, practical exposure, and hard work in building a successful career in technology.

- The session was formally concluded with a **heartfelt vote of thanks** presented by the IEEE PES MSIT team. The department expressed sincere gratitude to Mr. Vaibbhav Mishra for sharing his valuable time and knowledge with the students.

- The seminar witnessed enthusiastic participation from a large number of students across the ECE and EEE departments. The positive feedback and engagement during the event reflected the students' eagerness to explore hardware design as a career path. The session not only enhanced their technical understanding but also inspired them to pursue excellence in the field of electronics.

ATTENDANCE

Total – 120+
IEEE Members – 15+
Non-IEEE Members – 100+

ORGANIZING COMMITTEE

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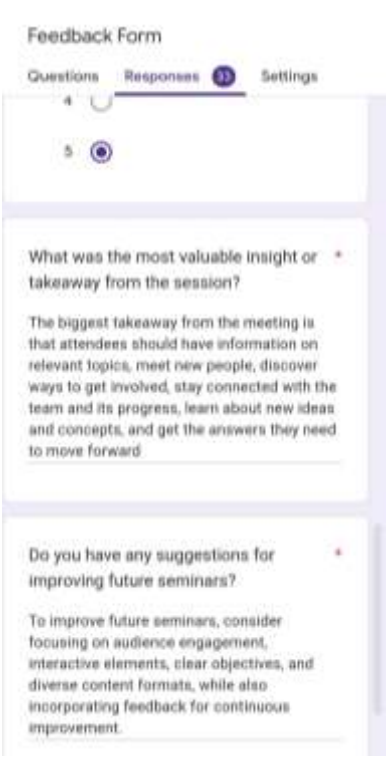
PHOTOGRAPHS OF THE EVENT


Feedback Form
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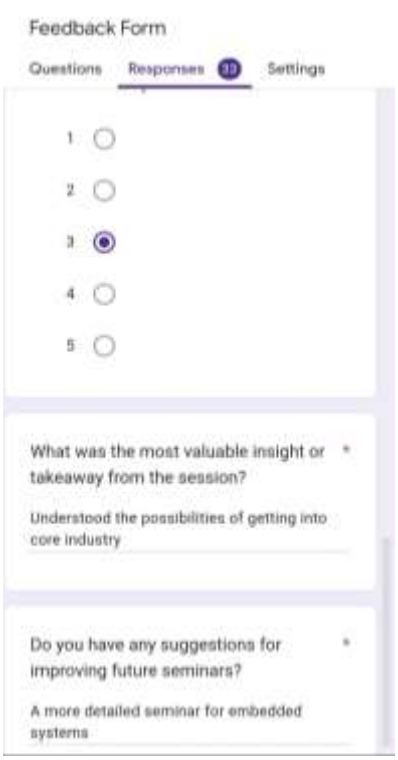
What was the most valuable insight or takeaway from the session? *
The biggest takeaway from the meeting is that attendees should have information on relevant topics, meet new people, discover ways to get involved, stay connected with the team and its progress, learn about new ideas and concepts, and get the answers they need to move forward
Do you have any suggestions for improving future seminars? *
To improve future seminars, consider focusing on audience engagement, interactive elements, clear objectives, and diverse content formats, while also incorporating feedback for continuous improvement.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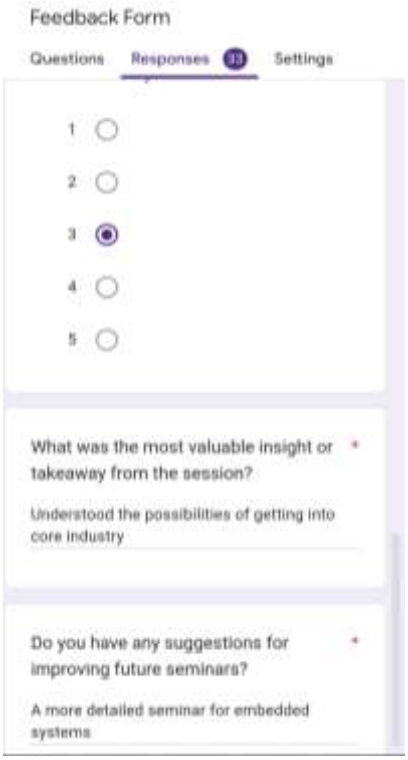
Understood the possibilities of getting into core industry
Do you have any suggestions for improving future seminars? *
A more detailed seminar for embedded systems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